



# S3 Trio64V+

# Integrated Graphics/Video Accelerator

# High-Performance Integrated Graphics/Video Accelerator

- High-performance DRAM-based 64-bit graphics engine
- Integrated 24-bit RAMDAC and programmable dual-clock synthesizer with 135 MHz output pixel rate
- Unique S3 Streams Processor for hardwareassisted video playback
- S3 Scenic Highway for direct interface to live video and MPEG-1 peripherals

### S3 Trio64-compatible Mode Option

#### **S3 Streams Processor Features**

- Supports on-the-fly stretching and blending of primary RGB stream and RGB or YUV (video) secondary stream
- Each stream can have different color depths
- YUV data is color space converted

## **Advanced Playback Capabilities**

- High-quality hardware-assisted video playback (up to 1024x768x16 bits/pixel)
- Support for Indeo, Cinepak, and softwareaccelerated MPEG-1 video playback

#### Game and Presentation Effects.

- Hardware double-buffering support for highquality tear-free playback
- 2-D scrolling and sprite plane support
- Color and chroma keying for overlaying of graphics onto video and video onto graphics
- Arithmetic blending of two pixel streams for fade-in/fade-out transition effects

#### S3 Scenic Highway Interface

- Philips SAA7110/SAA7111 video digitizers
- S3 Scenic/MX2 MPEG-1 audio/video decoder, C-Cube CL-480 MPEG-1 decoder

#### High Non-Interlaced Screen Resolution Support

- 1280x1024x256 colors at 75 Hz refresh
- 1024x768x64K colors at 75 Hz refresh
- 800x600x16.7M colors at 75 Hz refresh

### High-Performance Memory Interface

- 64-bit DRAM memory interface
- Supports 1-, 2-, or 4-MByte frame buffer
- Supports standard fast page mode and EDO DRAMs (60 MHz) and 1-cycle EDO DRAMs (50 MHz)

# Supports CPUs with Big or Little Endian Byte Ordering

#### Industry-Standard Local Bus Support

- Glueless PCI bus support (fully compliant with Revision 2.0)
- Glueless VESA<sup>®</sup> VL-Bus support

#### **Multimedia Support Hooks**

- Glueless 16-bit VESA Advanced Feature Connector (VAFC)
- 8-bit bidirectional feature connector
- S3 Scenic Highway
- I<sup>2</sup>C bus

#### Full Software Support

• Drivers for Windows<sup>®</sup> 3.11, Windows<sup>®</sup> NT, Windows<sup>®</sup> 95, OS/2<sup>®</sup> 2.1 and 3.0 (Warp<sup>™</sup>), SCO<sup>®</sup> UNIX<sup>®</sup>

#### Green PC/Monitor Plug and Play Support

- Full hardware and BIOS support for VESA Display Power Management Signaling (DPMS) monitor power savings modes
- DDC monitor communications support

# Extensive Static/Dynamic Power Management

#### Industry-Standard 208-pin PQFP package

© Copyright 1995, 1996 S3 Incorporated. All rights reserved. If you have received this document from S3 Incorporated in electronic form, you are permitted to make the following copies for business use related to products of S3 Incorporated: one copy onto your computer for the purpose of on-line viewing, and one printed copy. With respect to all documents, whether received in hard copy or electronic form, other use, copying or storage, in whole or in part, by any means electronic, mechanical, photocopying or otherwise, is not permitted without the prior written consent of S3 Incorporated, P.O. Box 58058., Santa Clara CA 95052-8058. S3 and True Acceleration are registered trademarks of S3 Incorporated. The S3 Corporate Logo, S3 on Board, S3 on Board design, S3d design, Vision968, Trio, Trio64, Trio64V+, Trio64UV+, VIRGE, VIRGE, VIRGE/VX, S3d, Scenic, Scenic/MX1, Scenic/MX2, Scenic Highway, Sonic, Sonic/AD, Aurora64V+, DuoView, Cooperative Accelerator Architecture, Streams Processor, MIC, Galileo, Native-MPEG, No Compromise Integration, No Compromise Acceleration and Innovations in Acceleration are trademarks of S3 Incorporated. Other trademarks referenced in this document are owned by their respective companies. The material in this document is for information only and is subject to change without notice. S3 Incorporated reserves the right to make changes in the product design without reservation and without notice to its users.



# Integrated Graphics/Video Accelerator

#### Overview

The S3<sup>®</sup> Trio64V+™ integrated graphics/video accelerator (hereinafter referred to as the Trio64V+) combines high-performance graphics and high-quality video acceleration features with the capability to directly interface to live video and MPEG-1 peripherals. It incorporates an 64-bit graphics enhanced version of the accelerator core and high-performance 135 MHz true-color RAMDAC that are found in S3's existing Trio64™ accelerators. All display applications that require high-quality video playback (from a CD-ROM or hard drive), or live video input capability, can take advantage of the Trio64V+'s new features. The Trio64V+ accelerates/ enhances software MPEG-1/Indeo/Cinepak video playback by providing arbitrary scaling with high-quality linear interpolation and color space conversion (RGB to YUV). By performing these tasks in hardware and relieving the CPU of a substantial overhead, the Trio64V+ offers high-quality video playback with window sizes of up to 1024x768x16 bits/pixel at high frame rates. The Trio64V+ has an S3 Scenic Highway™ interface that provides a direct interface to MPEG-1 audio/video decoder devices and live video digitizers to attain fullmotion video.

#### **S3 Streams Processor**

The S3 Streams Processor<sup>™</sup> allows the mixing of three separate display streams. The primary stream can be RGB data of any color depth. The secondary stream can be RGB or YUV (video) data of any color depth. YUV data is color space converted to RGB. The third stream, the hardware cursor, overlays the other two streams.

Arithmetic blending of a primary graphics stream and secondary graphics/video enables dramatic transition effects for game applications. Color and chroma keying allow opaque or transparent overlays of one stream on the other. Hardwareassisted double buffering of both primary and secondary data streams is also provided to enable high-quality "tear-free" playback.

The Trio64V+ also enhances game acceleration, with support for a sprite plane where sprites are actually rendered into a sprite plane memory.

Sprites can be overlaid onto the background without saving and restoring the background.

The Streams Processor is located in the pixel datapath between the display memory and the RAMDAC that drives the RGB signals to the monitor. One of the key advantages of this architecture is that it permits processing of pixel streams on the fly at display refresh rates. This eliminates the need to first write back processed (scaled or color-space-converted) data into the frame buffer before sending it to the RAMDAC. This saves memory storage and memory bandwidth.

The Streams Processor also enables simultaneous display of graphics and video of different color depths. For example, it is possible to display 24 bits/pixel-equivalent video on top of an 8-bit graphics background. This also saves memory bandwidth and storage capacity while permitting higher frame rates because of reduced bandwidth requirements.

In addition, if an opaque rectangular window of one stream is overlaid onto another background window of a second stream, it is not necessary to fetch and refresh the hidden pixels. This provides additional memory bandwidth savings.

#### S3 Scenic Highway

The S3 Scenic Highway interface directly connects to MPEG-1 audio/video decoders such as the S3 Scenic/MX2<sup>™</sup> MPEG-1 audio/video decoder and the C-Cube<sup>®</sup> CL-480 as well as video digitizers such as Philips<sup>®</sup> 7110/7111. This provides easy implemen-tation of MPEG-1 or digital video daughtercards that directly plug into the Scenic Highway connector or, alternately, ISA cards, where a ribbon cable is also necessary.

The Streams Processor and Scenic Highway are tightly coupled to provide optimal live video playback. The hardware automatically switches capture and display buffers without software intervention.